Sem V (2008 Scheme) B.Tech Practical Examination Branch: 412-Electronics and Communication Engineering Paper: 08.508-Digital Signal Processing Lab

Schedule

Sem V (2008 Scheme) Jan_2019 Examination40Sree Chithira Thirunal College 29Branch: 412- Electronics and Communication40University College of 4(402) Sree Chithira ThiruvananthapuramBranch: 412- Electronics and Communication41Muslim Association College of Thiruvananthapuram7041Muslim Association College of Pappanamcode27/08 Pappanamcode27/08 Pappanamcode
Engineering41Muslim Association College of 5Pappanamcode, ThiruvananthapuramPaper: 08.507- Digital Signal Processing Lab42College of Engineering and 0Wanagement, Vadakkal P O, Punnapra, AlappuzhaPappanamcode, Thiruvananthapuram

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